

1050 U.S. PTO  
10/082160  
02/26/02

PATENT NUMBER and  
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10082160	FILING DATE 02/26/2002	CLASS 438	SUBCLASS 14	GAU 2812	EXAMINER THAO LE
<b>**APPLICANTS:</b> Takahashi Youji; Iida Tsutomu; Umemoto Tsuyoshi; Kashibe Makoto;					
<b>**CONTINUING DATA VERIFIED:</b>					
<b>** FOREIGN APPLICATIONS VERIFIED:</b>					
PG-PUB DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO			
35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no		500.41283X00			
Verified and Acknowledged Examiners's initials					
TITLE : Power supply, a semiconductor making apparatus and a semiconductor wafer fabricating method using the same					
U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)					

<b>NOTICE OF ALLOWANCE MAILED</b>		<b>CLAIMS ALLOWED</b>	
		Assistant Examiner	
<b>ISSUE FEE</b>		<b>DRAWING</b>	
Amount Due	Date Paid	Total Claims	
		Print Claim for O.G.	
		Sheets Drwg.	
		Figs. Drwg.	
		Print Fig.	
<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>		Primary Examiner	
		Applicati n Examiner	
		<b>PREPARED FOR ISSUE</b>	
<b>WARNING:</b> The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368, Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.			

FILED WITH:

☐ DISK (CRF)

☐ CD-ROM

(Attached in pocket on right inside flap)